



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of

YONEMARU, M.

Atty. Ref.: 829-618; Confirmation No. 3114

Appl. No. 10/720,764

TC/A.U. 2826

Filed: November 25, 2003

Examiner: Dickey, T.

For: SEMICONDUCTOR INTEGRATED CIRCUIT AND FABRICATION METHOD FOR  
SAME

\* \* \* \* \*

July 5, 2005

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**AMENDMENT**

Responsive to the Official Action dated March 4, 2005 (for which petition is hereby made for a one month extension of time), please amend the above-identified application as follows:

YONEMARU, M.  
Appl. No. 10/720,764  
July 5, 2005

**AMENDMENTS TO THE TITLE:**

*Please amend the title as follows:*

SEMICONDUCTOR INTEGRATED LOGIC CIRCUIT AND FABRICATION  
METHOD FOR SAME